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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/569,010	10/18/2006	Gerald Eckl	FRZ-107US	1745
23122	7590	02/04/2009	EXAMINER	
RATNERPRESTIA			BERHANE, ADOLF D	
P.O. BOX 980				
VALLEY FORGE, PA 19482			ART UNIT	PAPER NUMBER
			2838	
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			02/04/2009	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/569,010	Applicant(s) ECKL ET AL.
	Examiner Adolf Berhane	Art Unit 2838

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED. (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(o).

Status

- 1) Responsive to communication(s) filed on _____.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-11 is/are pending in the application.
 - 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) Claim(s) ____ is/are allowed.
- 6) Claim(s) 1-4 and 7 is/are rejected.
- 7) Claim(s) 5, 6 and 8-11 is/are objected to.
- 8) Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on ____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO/SB/08e)
 Paper No(s)/Mail Date _____
- 4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date _____
- 5) Notice of Informal Patent Application
- 6) Other: _____

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

2. The Examiner has considered the information disclosure statement (IDS) submitted on 2/21/06.

Claim Objections

3. Claims 1, 2 and 3 are objected to because of the following informalities: Claim 1, line 10, claim 2, line 2 and claim 3, line 3 "(bus) interface" should be stated as -- interface bus-- of just --interface--. Appropriate correction is required.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 1-4 and 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Brunk et al. (5,272,614).

Brunk et al. discloses a microprocessor-controlled DC-DC converter in Figs. 1 and 4. A voltage converter (1,T1, TR2) for converting a primary/secondary voltage into a secondary/primary voltage (U_E/U_V), at least one controlled switch (13,T1), control

circuit (7) according to its supplied set points (Df), the at least one controlled switch (T1) with a variable pulse duty factor and/or variable control times and/or variable frequency, a digital signal processor (12) for calculation of the set points for the control circuit (7), a voltage converter comprises an interface (S), via which operating parameters (Ua, Ui) can be transmitted to the digital signal processor and the preset from an external control center (source of U_{ref}), memory (9), coarse quantizer (5) and digital filter (4).

6. Claims 1-4 and 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Goodarzi et al. (6,538,909).

Goodarzi et al. disclose a universal high efficiency power converter in Figs. 1 and 2. A voltage converter (10/100) for converting a primary/secondary voltage into a secondary/primary voltage (110/220), at least one controlled switch (142,144,152,154), control circuit (34) according to its supplied set points (128,132), the at least one controlled switch (142,144,152,154), with a variable pulse duty factor and/or variable control times and/or variable frequency, a digital signal processor (136) for calculation of the set points for the control circuit (7), a voltage converter comprises an interface (196,200), via which operating parameters (182) can be transmitted to the digital signal processor and the preset from an external control center (source of 134) and memory (136).

Allowable Subject Matter

7. Claims 5, 6 and 8-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adolf Berhane whose telephone number is 571-272-2077. The examiner can normally be reached on Monday- Friday 8 AM to 6 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Akm E. Ullah can be reached on 571-272-2361. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Adolf Berhane/
Adolf Berhane
Primary Examiner
Art Unit 2838

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